

April 9-10, 2024 – Gardanne, Aix-Marseille area, France

# CALL FOR PAPERS

### THE 15<sup>TH</sup> INTERNATIONAL WORKSHOP ON "CONSTRUCTIVE SIDE-CHANNEL ANALYSIS AND SECURE DESIGN"

## April 9-10, 2024, Gardanne, Aix-Marseille area, France

Workshop website for more information: https://www.cosade.org/cosade24/

**Side-channel analysis (SCA) and implementation attacks** have become an important field of research and a real threat. In order to enhance the resistance of cryptographic and security critical implementations within the design phase, constructive attacks and analysing techniques may serve as a quality metric to optimize the design and development process. Since 2010, COSADE provides an international platform for researchers, academics, and industry participants to present their work and their current research topics. The 15<sup>th</sup> International Workshop on Constructive Side-Channel Analysis and Secure Design will be organized by Mines Saint-Etienne and will be held at the Center of Microelectronics in Provence, Gardanne, France.

The program committee is seeking original papers on all aspects of the side-channel analysis and other implementation attacks as well as efficient and secure implementations. You are invited to participate and submit your contributions to COSADE'24. The workshop's submission topics include, but are not limited to:

- Implementation attacks & countermeasures: Side-channel analysis, fault-injection attacks, probing and read-out, hardware trojans, cloning and counterfeiting, side-channel or fault-injection based reverse engineering, attacks or countermeasures based on machine learning methods
- Efficient and secure HW/SW implementations: Efficient and secure implementations of cryptographic blocks including post-quantum cryptography, lightweight cryptography, random number generators, physical unclonable functions (PUFs), symmetric cryptography, hash functions, leakage-resilient cryptography, fault-resistant and tamper-detection designs, white-box cryptography
- Hardware-intrinsic security: Foundations and practical aspects of hardware-intrinsic security, use of instance-specific and process-induced variations in electronic devices for cryptography, novel PUF designs, hardware-intrinsic security threats, supply-chain protection
- Measurement setups, evaluation platforms, and open benchmarks: Practical implementation and comparison of physical attacks including description of measurement setups, test platforms for evaluation of physical attacks, open benchmarks for physical attacks and countermeasures
- Formal analysis and automated tools: Security and leakage models, formal analysis of secure implementations, design automation and tools, evaluation tooling, domain-specific security analysis of, e.g., IoT, medical, automotive, industrial-control systems, 5G, etc.

#### **Submissions Instructions**

Submitted papers must be original, unpublished, anonymous, and not submitted to journals or other conferences/workshops that have proceedings. Submissions must be written in English, strictly follow Springer LNCS format (with default margins, font size, etc.) and should be at most 18 pages, excluding only references. All submissions will be blindrefereed. Submission implies the willingness of at least one of the authors to register and present the paper.

The proceedings will be published in the Springer Lecture Notes in Computer Science (LNCS) series. Accepted papers must follow the LNCS author instructions at: <u>https://www.springer.com/gp/computer-science/lncs/conference-proceedings-guidelines</u>

Important Dates: All deadlines are 23:59:59 Anywhere on Earth (AoE).	
Abstract submission deadline:	December 15, 2023 (Extended)
Paper submission deadline:	December 22, 2023 (Extended)
Rebuttal phase:	January 15-19, 2024
Notification of acceptance:	January 26, 2024
Camera-ready version:	February 12, 2024

Submission Website

Authors are invited to submit papers (PDF format) electronically using the submission form available on https://easychair.org/conferences/?conf=cosade2024

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