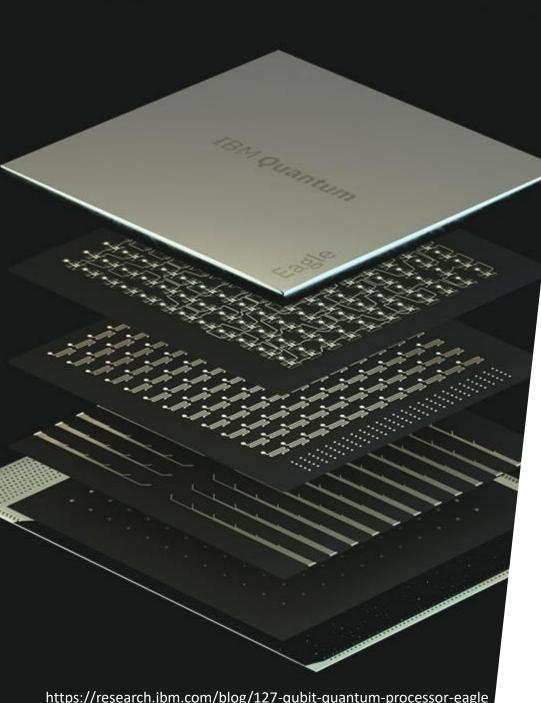


#### COSADE 2022, KU Leuven, Belgium

Tuesday, 12 April 2022

Single-trace Clustering Power Analysis of The Point Swapping Procedure in the Three Point Ladder of Cortex-M4 SIKE

Aymeric Genêt, Novak Kaluđerović



## Introduction

#### Quantum computers

IBM Eagle: 127-qubit quantum computer lacksquare

#### Post-quantum cryptography

Resists quantum computing (in theory) 

#### Side-channel attacks

Attacks on electronic devices 



### Outline

### **1.** Supersingular isogeny key exchange (SIKE)

## 2. Clustering power analysis of SIKE

#### **3.** Countermeasure

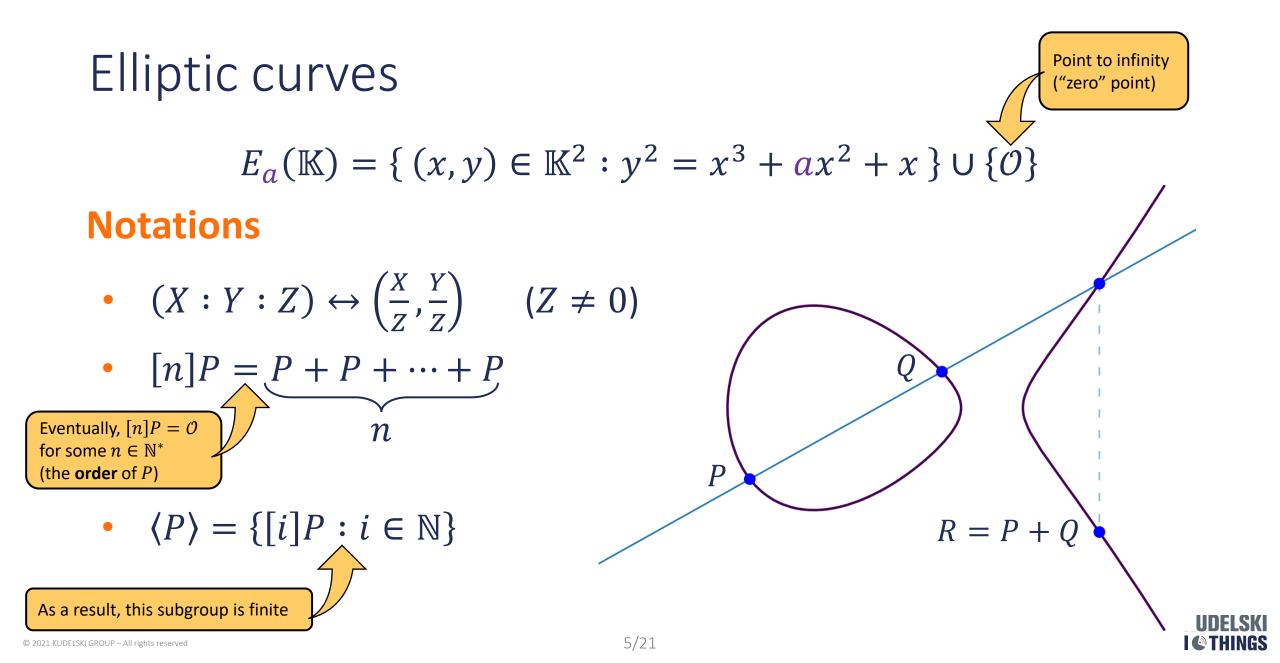




Section 1

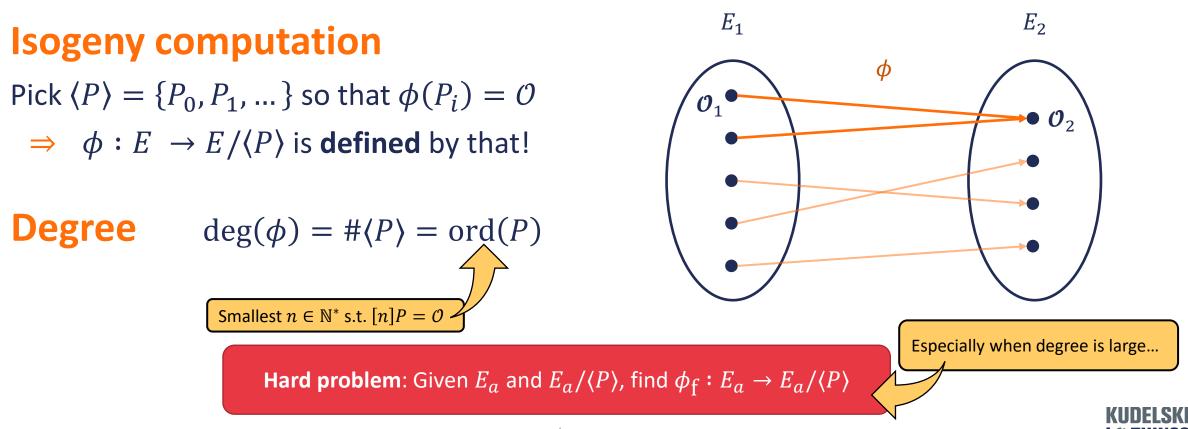
## Supersingular isogeny key exchange (SIKE)





#### Isogenies

#### **Isogeny**: surjective <u>mapping</u> of <u>finite kernel</u> between two <u>elliptic curves</u>



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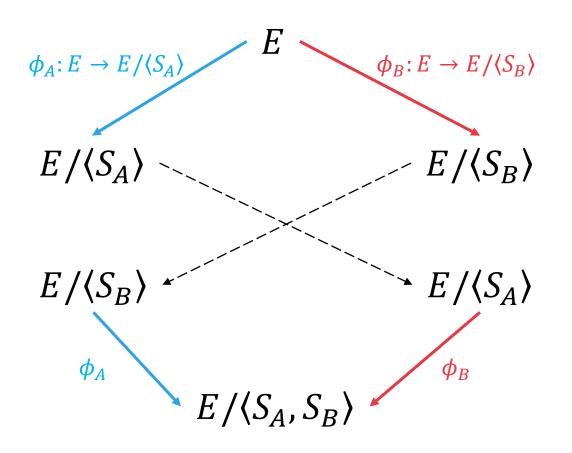
## Supersingular isogeny Diffie-Hellman (SIDH)

#### **Party computations**

1. 
$$S = P + [sk]Q$$
 of order  $\begin{cases} 2^{e_A} (Alice) \\ 3^{e_B} (Bob) \end{cases}$ 

- 2. Obtain  $\phi$  from  $\langle S \rangle$
- **3.** Send  $E/\langle S \rangle, \phi(P), \phi(Q)$

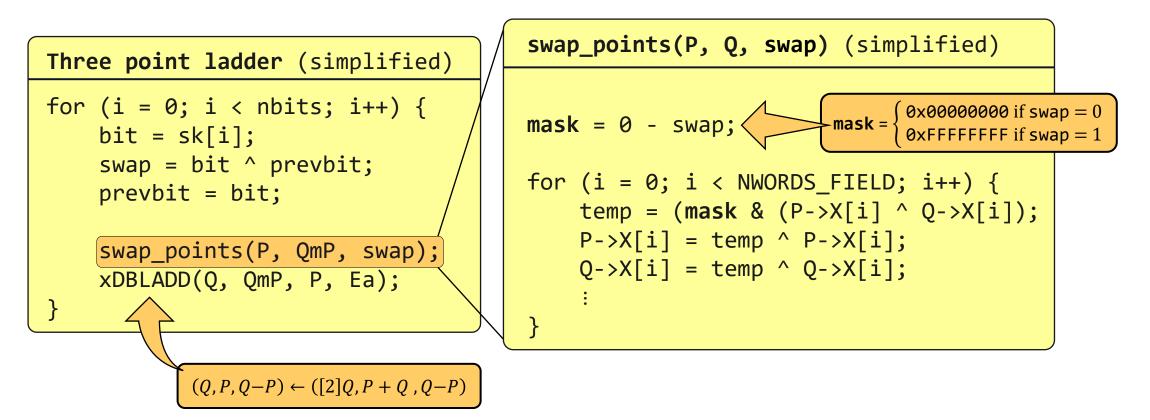






## Three point ladder

Given (Q, P, Q - P), compute efficiently S = P + [sk]Q on  $E_a$ 







#### Section 2

## Clustering power analysis of SIKE



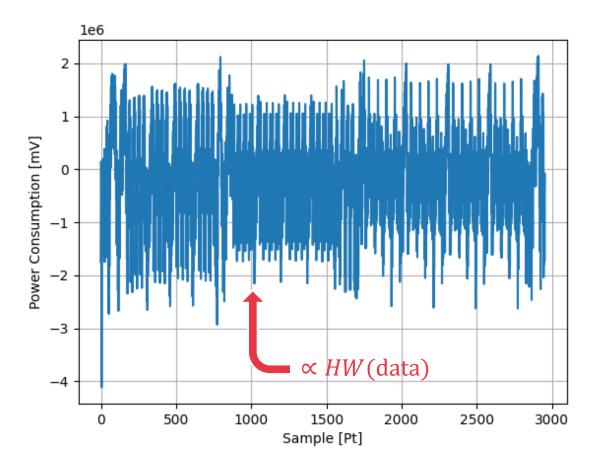
## Side-channel power analysis

Passive known-text attack

## Power consumption linked to **processed data**

Exploit link to recover secrets

- Simple power analysis
- Differential power analysis



Example of a **power trace** 



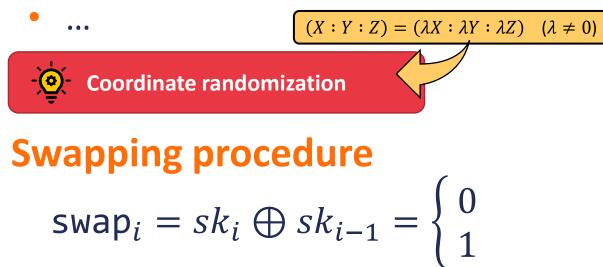
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Power analysis of SIKE

Attack on the ECC part of SIKE

#### **Double-and-add procedure**

- Correlation power analysis
- Template attack



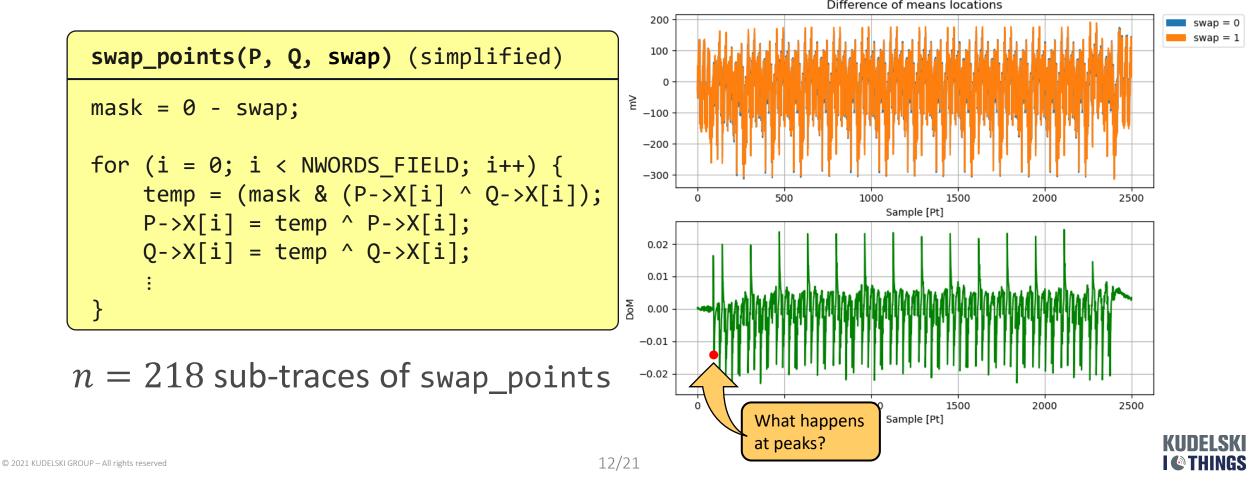
#### A single trace contains the n iterations

```
Three point ladder (simplified)
for (i = 0; i < nbits; i++) {
   coord_randomize(Q, P, QmP);
   bit = sk[i];
   swap = bit ^ prevbit;
   prevbit = bit;
   swap_points(P, QmP, swap);
   xDBLADD(Q, QmP, P, Ea);
}</pre>
```

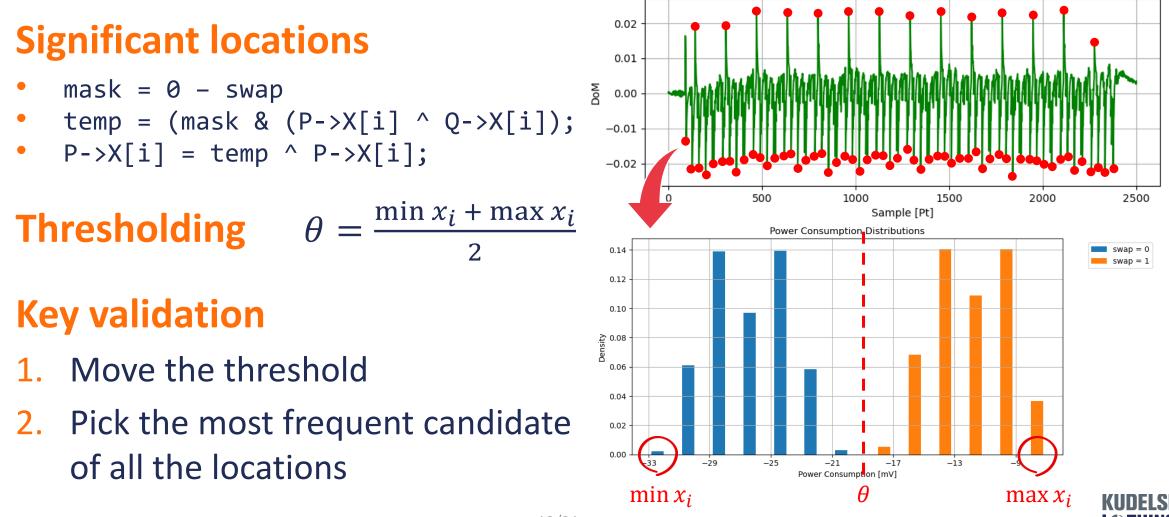


#### swap\_points Difference of Means (swap=0 vs. swap=1)

ChipWhisperer (29.54 [MHz]) + LNA (20dB) on STM32F3 (7.37 [MHz])



### Single-sample swap\_points leakage





## Clustering power analysis

Cluster relative **power samples** according to their **secret value** [HIM+13, PITM14, NaC17,...]

#### Methodology (Perin et al. 2014)

- 1. Leakage assessment (k-means)
- 2. Points of interest selection
- 3. Key recovery (fuzzy *k*-means)
- 4. Key validation

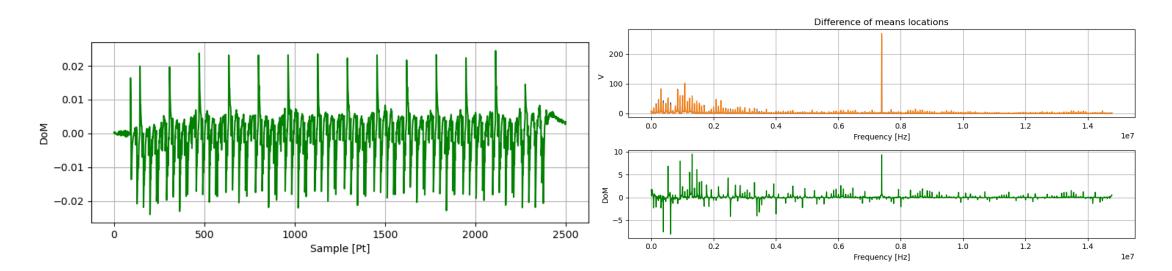
#### k-means algorithm

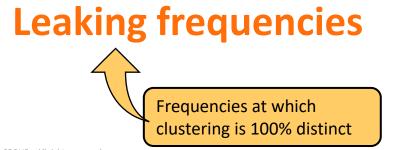
- **Input**:  $\{x_i \in \mathbb{R}\}$  Collection of samples
- 1. Assign  $x_i$  to cluster  $0 \le j < k$  at random
- 2. repeat
- 3. Compute the mean of all clusters  $\mu_i$
- 4. Assign  $x_i$  to cluster  $j = \operatorname{argmin}_{0 \le j < k} |x_i \mu_j|$
- 5. **until** no  $\mu_j$  changes
- 6. **return** final cluster arrangement of  $x_i$

k-means is used with k = 2(i.e., swap = 0, swap = 1)

## Clustering power analysis in **frequency**

Pre-process traces with a Fourier transform





0.74 [MHz], 0.93 [MHz], 2.41 [MHz], 3.33 [MHz], 4.07 [MHz], 8.13 [MHz], 8.32 [MHz], 10.72 [MHz], 11.46 [MHz]

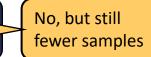
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## Clustering power analysis with wavelet

Does the success rate improve with isolated frequency band?

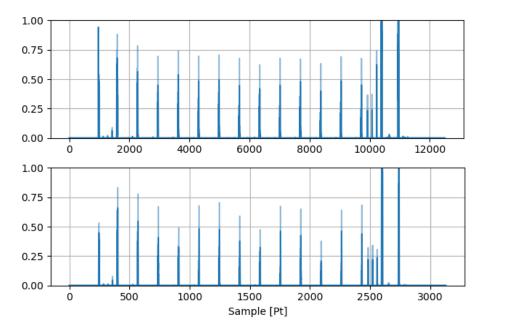
 $\Leftrightarrow$ 

 $\Leftrightarrow$ 

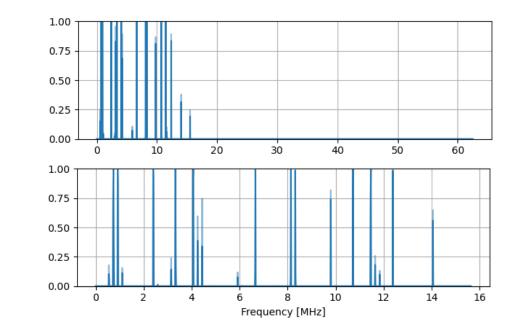


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#### Wavelet transform as a filter to keep lower frequencies of signal



#### Success rate of clustering (at each timing)



Success rate of clustering (at each frequency)



Section 3

## Countermeasure



## Secure swap\_points

Suppose *a*, *b* are words that need to be swapped depending on swap

#### **Before**

1. mask = 
$$\begin{cases} 0 \times 00000000 \text{ if swap} = 0\\ 0 \times \text{FFFFFFF if swap} = 1 \end{cases}$$

2. temp = mask &  $(a \oplus b)$ 

3. 
$$a = \text{temp} \oplus a$$

4.  $b = \text{temp} \oplus b$ 

After  $\begin{array}{c}
\mathbf{m1} \oplus \mathbf{m2} = \begin{cases} 0 \times 00000000 \text{ if swap} = 0\\ 0 \times \mathsf{FFFFFFF} \text{ if swap} = 1 \end{cases}$ 1. Draw m1, m2 uniformly s.t. m2 =  $\begin{cases} \mathbf{m1} & \text{if swap} = 0\\ \neg \mathbf{m1} & \text{if swap} = 1 \end{cases}$ 2. temp1 = m1 & ( $a \oplus b$ ) 3. temp2 = m2 & ( $a \oplus b$ ) 4.  $a = (\text{temp1} \oplus a) \oplus \text{temp2}$ 5.  $b = (\text{temp1} \oplus b) \oplus \text{temp2}$ 



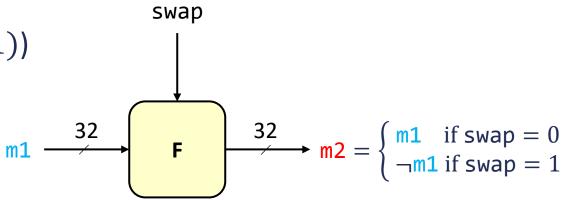
## Secure mask computation

How do you obtain such m1 and m2?

⇒ **Two's complement** (i.e.,  $\neg x = -(x + 1)$ )

#### Instructions

- 1. u1 = randombytes(4) & 0xFFFFFFD
- 2. m1 = randombytes(4) & 0xFFFFFFE
- 3.  $u^2 = u^1 + swap$
- 4. r = m1 + swap
- 5. u1 = u1 + 1
- $6. \quad u1 = u1 \times r$
- 7.  $u^2 = u^2 + swap$
- 8.  $u^2 = u^2 \times r$
- 9.  $m^2 = u^1 u^2$



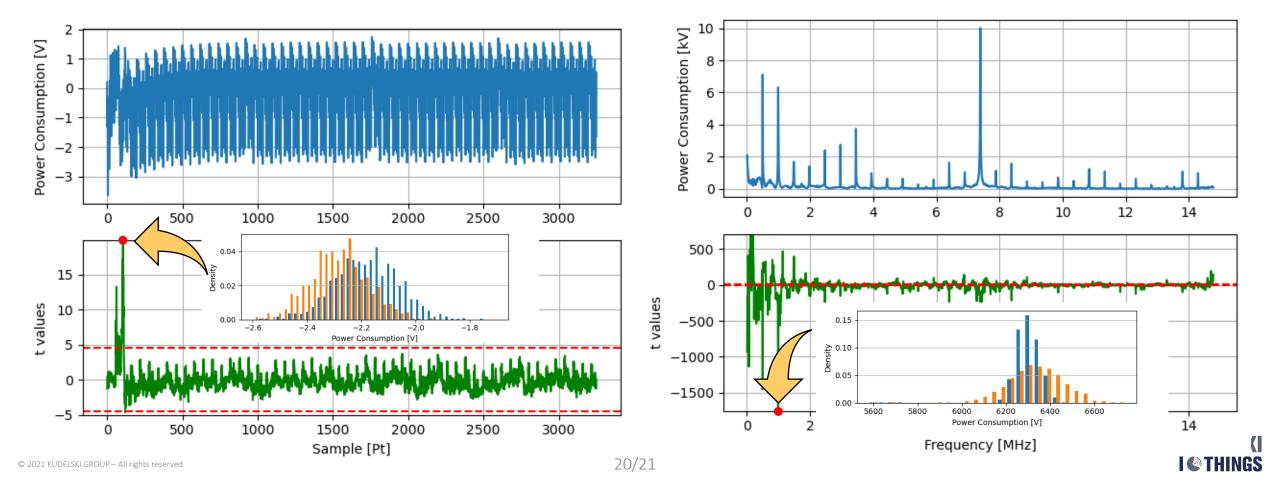
$$m2 = (m1 + swap)(1 - 2 \cdot swap)$$
$$= u1(m1 + swap) - u2(m1 + swap)$$

where 
$$u1 - u2 = 1 - 2 \cdot swap$$



## *t*-test (swap=0 vs. swap=1, N = 1000)

ChipWhisperer (29.54 [MHz]) + LNA (20dB) on STM32F3 (7.37 [MHz])



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