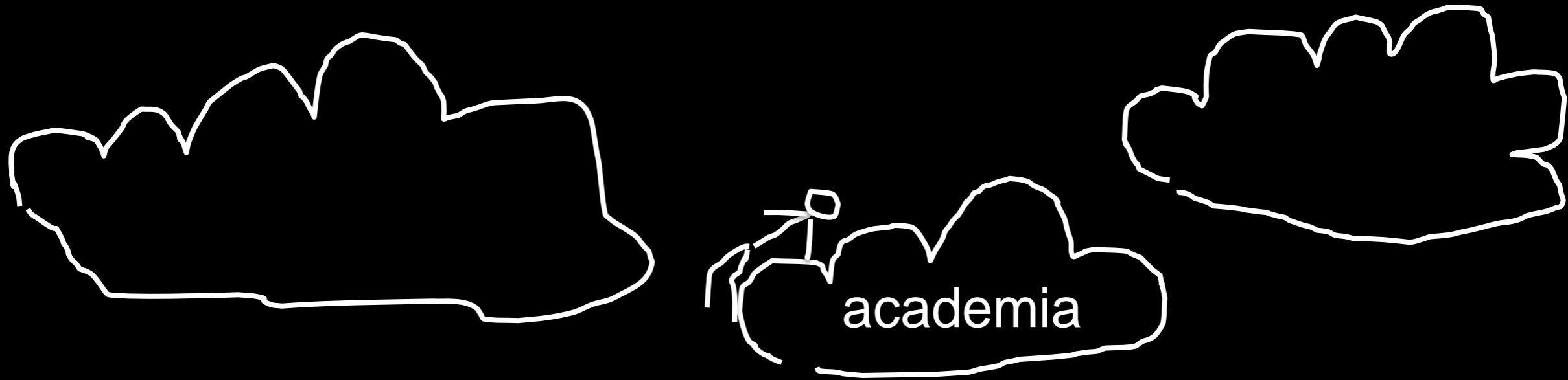


# **riscure**

**Academic vs. industrial  
perspective on SCA...  
...and an industrial  
innovation**

Ilya Kizhvatov, Marc Witteman

COSADE 2013, Paris



industry

disclaimer: this picture is the private opinion of the speaker

**How many of you  
have published a paper on SCA  
in the recent year?**

**Do you think  
your results  
are practical?**

**Out of dozens of SCA papers  
we as a test lab  
find that few are practical.**

**Why?**

**1. Limited testing time**

**and SCA only part of it**

## **2. Large amounts of traces**

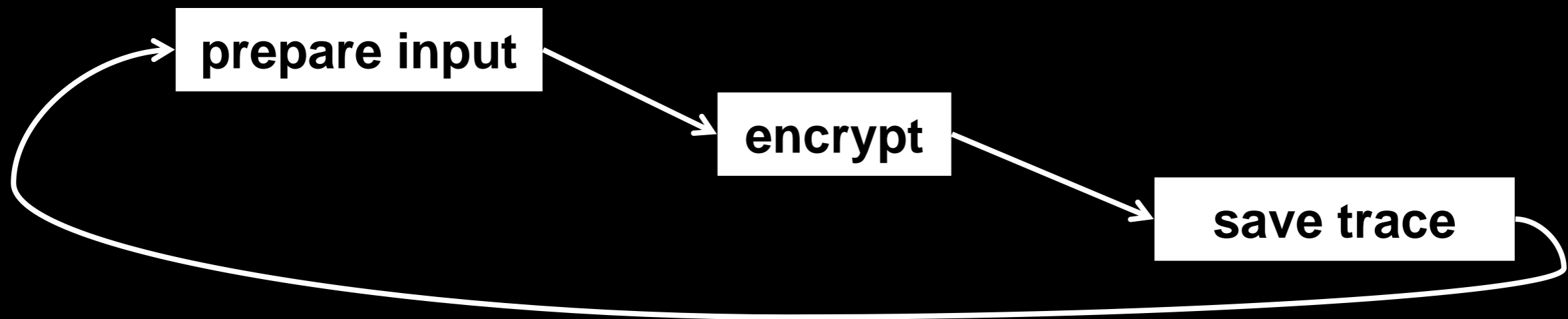
**Compute success rate, you say?**



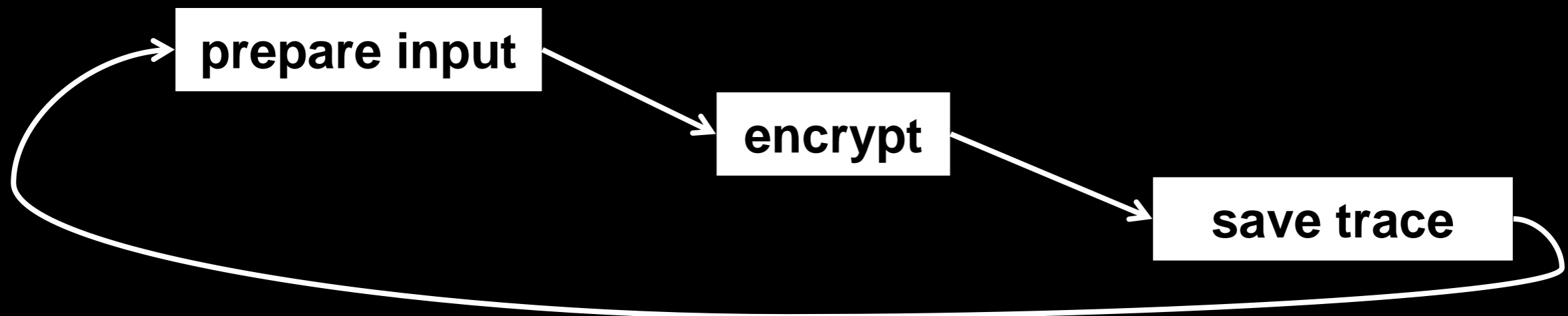
**What do we do?**

**We use “fast acquisition” with  
averaging on the DSO.**

# Common acquisition flow

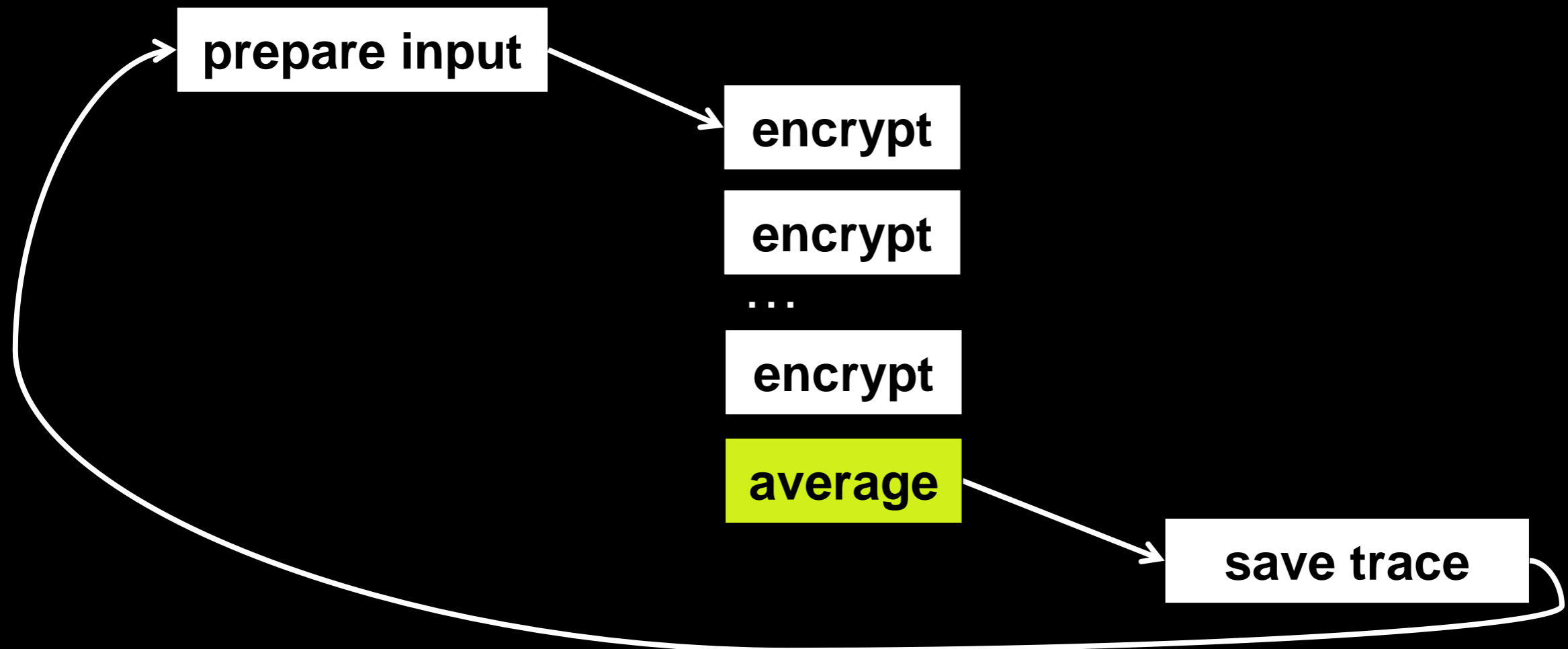


# Common acquisition flow



---

# Fast acquisition flow with averaging



**No slightest academic value.**

**Effect?**

**Up to 200 times speed up.**

**Requires great degree of control  
over the target → reject (-1)**

**Requires great degree of control  
over the target → reject (-1)**

---

**Runtime control  
is a standard prerequisite  
in embedded scenarios**

**Will not work**

**with jitter  $\rightarrow$  reject (-2)**



**Will not work  
with jitter → reject (-2)**

---

**Well it works,  
we align traces on the DSO**

**Will not work**

**against masking  $\rightarrow$  reject (-2)**

**confidence 4!**

**Will not work**

**against masking  $\rightarrow$  reject (-2)**

**confidence 4!**

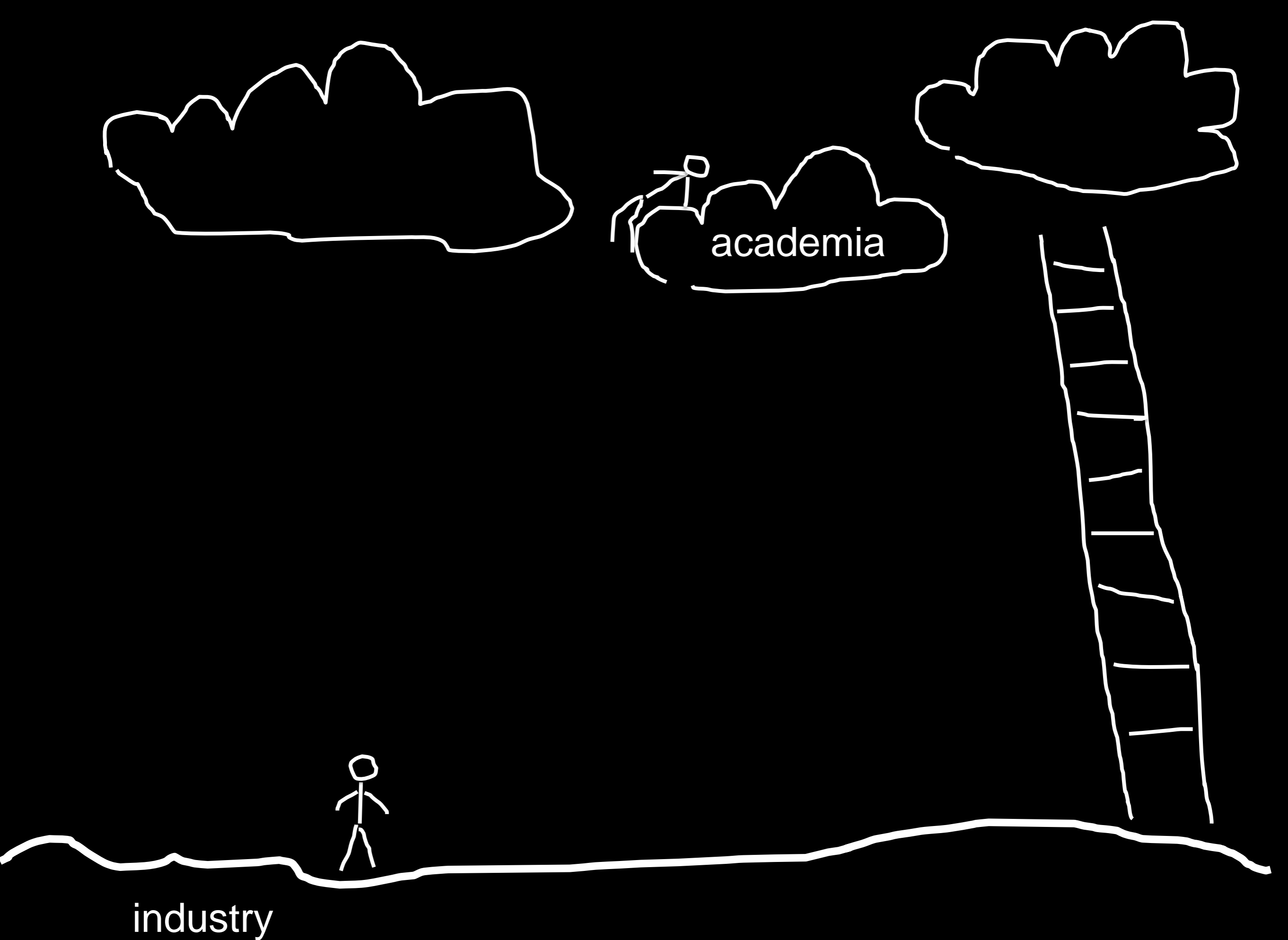
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**Think of residual**

**first order leakage.**

**Full white paper on fast acquisition**

**[www.riscure.com/news-events](http://www.riscure.com/news-events)**



industry

academia

disclaimer: this picture is the private opinion of the speaker

# riscure

# Challenge your security

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